

Day : Monday
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Time: 15:23:07

Inventor Name Search Result

Your Search was:

Last Name = IWAMATSU

First Name = TAKANORI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>07063479</u>	4788696	150	06/18/1987	DECISION TIMING CONTROL CIRCUIT	IWAMATSU, TAKANORI
<u>07066960</u>	4860010	150	06/29/1987	AUTOMATIC DRIFT CONTROL CIRCUIT	IWAMATSU, TAKANORI
<u>07156755</u>	4860316	150	02/18/1988	MULTILEVEL AMPLITUDE MODULATION AND DEMODULATION COMMUNICATION SYSTEM	IWAMATSU, TAKANORI
<u>07245216</u>	4914676	250	09/16/1988	CROSS POLARIZATION INTERFERENCE CANCELLER	IWAMATSU, TAKANORI
<u>07247444</u>	4912726	150	08/31/1988	DECISION TIMING CONTROL CIRCUIT	IWAMATSU, TAKANORI
<u>07265459</u>	5005186	150	10/13/1988	DIGITAL DEMODULATOR APPARATUS	IWAMATSU, TAKANORI
<u>07909712</u>	5406589	150	07/07/1992	FRACTIONALLY SPACED CROSS-POLARIZATION INTERFERENCE CANCELLER	IWAMATSU, TAKANORI
<u>08089786</u>	5400366	150	07/09/1993	QUASI-SYNCHRONOUS DETECTION AND DEMODULATION CIRCUIT AND FREQUENCY DISCRIMINATOR USED FOR THE SAME	IWAMATSU, TAKANORI
<u>08122530</u>	5495501	150	09/28/1993	COMMUNICATION SYSTEM INCLUDING A DIGITAL ROLL-OFF FILTER	IWAMATSU, TAKANORI
<u>08263686</u>	5852629	150	06/22/1994	REPEATER	IWAMATSU, TAKANORI
<u>08337243</u>	Not Issued	166	11/10/1994	QUASI-SYNCHRONOUS DETECTION AND DEMODULATION CIRCUIT	IWAMATSU, TAKANORI

				AND FREQUENCY DISCRIMINATOR USED FOR THE SAME	
<u>08526766</u>	<u>6201841</u>	150	09/11/1995	DISTORTION COMPENSATING DEVICE	IWAMATSU, TAKANORI
<u>08533119</u>	<u>5978415</u>	150	09/25/1995	AUTOMATIC AMPLITUDE EQUALIZER	IWAMATSU, TAKANORI
<u>08545883</u>	<u>5661761</u>	150	10/20/1995	QUASI-SYNCHRONOUS DETECTION AND DEMODULATION CIRCUIT AND FREQUENCY DISCRIMINATOR USED FOR THE SAME	IWAMATSU, TAKANORI
<u>08552543</u>	<u>5867542</u>	150	11/03/1995	CLOCK PHASE DETECTING CIRCUIT AND CLOCK REGENERATING CIRCUIT EACH ARRANGED IN RECEIVING UNIT OF MULTIPLEX RADIO EQUIPMENT	IWAMATSU, TAKANORI
<u>08565732</u>	<u>5594389</u>	150	11/30/1995	CARRIER REGENERATION CIRCUIT INCLUDING AREA JUDGING DEVICE	IWAMATSU, TAKANORI
<u>08573075</u>	<u>5648988</u>	150	12/15/1995	COMMUNICATION SYSTEM INCLUDING A DIGITAL ROLL-OFF FILTER	IWAMATSU, TAKANORI
<u>08607093</u>	<u>5920595</u>	150	02/28/1996	INTER-CROSS WAVE COMPENSATION METHOD AND APPARATUS PERFORMING FREQUENCY CONVERSION ON SIGNED NOT DETECTED BY A DEMODULATING UNIT.	IWAMATSU, TAKANORI
<u>08638317</u>	<u>5594759</u>	150	04/26/1996	QUASI-SYNCHRONOUS DETECTION AND DEMODULATION CIRCUIT AND FREQUENCY DISCRIMINATOR USED FOR THE SAME	IWAMATSU, TAKANORI
<u>08697966</u>	<u>5872812</u>	150	09/05/1996	CARRIER REPRODUCING CIRCUIT INCLUDING REGION DECIDING CIRCUITRY	IWAMATSU, TAKANORI
<u>08715023</u>	<u>5956374</u>	150	09/17/1996	JITTER SUPPRESSING CIRCUIT	IWAMATSU, TAKANORI
<u>08764969</u>	<u>5781076</u>	150	12/13/1996	DIGITAL QUADRATURE	IWAMATSU,

				AMPLITUDE MODULATORS	TAKANORI
<u>08794370</u>	<u>5844950</u>	150	02/04/1997	CROSS POLARIZATION INTERFERENCE CANCELER AND CROSS POLARIZATION INTERFERENCE ELIMINATING APPARATUS USING THE SAME	IWAMATSU, TAKANORI
<u>08828419</u>	<u>6028902</u>	150	03/28/1997	CLOCK PHASE DETECTING CIRCUIT	IWAMATSU, TAKANORI
<u>08961201</u>	<u>6034564</u>	150	10/30/1997	DEMODULATOR USING QUASI-SYNCHRONOUS DETECTION TO DEMODULATE MODULATED QUADRATURE INPUT SIGNALS	IWAMATSU, TAKANORI
<u>08962052</u>	<u>6236263</u>	150	10/31/1997	DEMODULATOR HAVING CROSS POLARIZATION INTERFERENCE CANCELING FUNCTION	IWAMATSU, TAKANORI
<u>08962054</u>	<u>6029056</u>	150	10/31/1997	SPACE DIVERSITY RECEIVING APPARATUS	IWAMATSU, TAKANORI
<u>09022955</u>	<u>6175591</u>	150	02/12/1998	RADIO RECEIVING APPARATUS	IWAMATSU, TAKANORI
<u>09057698</u>	<u>5987071</u>	150	04/09/1998	DIGITAL MODULATOR AND DIGITAL DEMODULATOR	IWAMATSU, TAKANORI
<u>09081352</u>	<u>6339623</u>	150	05/19/1998	REFERENCE CARRIER GENERATOR DEVICE FOR PULLING A REFERENCE CARRIER OUT OF A FALSE STABILIZED POINT INTO A CORRECT STABILIZED POINT OF SYNCHRONISM	IWAMATSU, TAKANORI
<u>09178305</u>	Not Issued	161	10/23/1998	CROSS POLARIZATION INTERFERENCE CANCELER AND CROSS POLARIZATION INTERFERENCE ELIMINATING APPARATUS USING THE SAME	IWAMATSU, TAKANORI
<u>09275757</u>	<u>6504879</u>	150	03/25/1999	DIGITAL MODULATION APPARATUS	IWAMATSU, TAKANORI
<u>09358619</u>	<u>6487236</u>	150	07/21/1999	METHOD AND APPARATUS FOR ACHIEVING DEMODULATION IN RADIO COMMUNICATIONS SYSTEM USING M-SEQUENCE ORTHOGONAL	IWAMATSU, TAKANORI

				MODULATION	
<u>09418453</u>	<u>6507625</u>	150	10/15/1999	DIGITAL MODULATOR AND DIGITAL DEMODULATOR	IWAMATSU, TAKANORI
<u>09516529</u>	<u>6690714</u>	150	03/01/2000	METHOD AND APPARATUS FOR ACHIEVING DEMODULATION IN RADIO COMMUNICATIONS SYSTEM USING M-SEQUENCE ORTHOGONAL MODULATION	IWAMATSU, TAKANORI
<u>09516986</u>	<u>6683906</u>	150	03/01/2000	RADIO COMMUNICATION APPARATUS	IWAMATSU, TAKANORI
<u>09543302</u>	<u>6707846</u>	150	04/05/2000	CORRELATION ENERGY DETECTOR AND RADIO COMMUNICATION APPARATUS	IWAMATSU, TAKANORI
<u>09584934</u>	Not Issued	161	05/31/2000	Soft decision metric generating circuit	IWAMATSU, TAKANORI
<u>09771229</u>	Not Issued	71	01/26/2001	CLOCK PHASE DETECTING CIRCUIT AND CLOCK REGENERATING CIRCUIT EACH ARRANGED IN RECEIVING UNIT OF MULTIPLEX RADIO EQUIPMENT	IWAMATSU, TAKANORI
<u>09784491</u>	<u>6487261</u>	150	02/15/2001	DISTORTION COMPENSATING DEVICE	IWAMATSU, TAKANORI
<u>10115470</u>	Not Issued	41	04/03/2002	Mobile communications receiving apparatus and method	IWAMATSU, TAKANORI
<u>10121776</u>	Not Issued	30	04/12/2002	Apparatus for estimating propagation path characteristics	IWAMATSU, TAKANORI
<u>10177075</u>	<u>6987814</u>	150	06/20/2002	DISTORTION COMPENSATING DEVICE	IWAMATSU, TAKANORI
<u>10611412</u>	Not Issued	30	06/30/2003	CDMA receiving device and method	IWAMATSU, TAKANORI
<u>11094783</u>	Not Issued	30	03/30/2005	OFDM transceiver apparatus	IWAMATSU, TAKANORI

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = KIYANAGI

First Name = HIROYUKI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>08408143</u>	<u>5596605</u>	150	03/20/1995	RADIO RECEIVER FOR USE IN THE RECEPTION OF DIGITAL MULTIPLEXING SIGNALS	KIYANAGI, HIROYUKI
<u>08533119</u>	<u>5978415</u>	150	09/25/1995	AUTOMATIC AMPLITUDE EQUALIZER	KIYANAGI, HIROYUKI
<u>08552543</u>	<u>5867542</u>	150	11/03/1995	CLOCK PHASE DETECTING CIRCUIT AND CLOCK REGENERATING CIRCUIT EACH ARRANGED IN RECEIVING UNIT OF MULTIPLEX RADIO EQUIPMENT	KIYANAGI, HIROYUKI
<u>08565732</u>	<u>5594389</u>	150	11/30/1995	CARRIER REGENERATION CIRCUIT INCLUDING AREA JUDGING DEVICE	KIYANAGI, HIROYUKI
<u>08828419</u>	<u>6028902</u>	150	03/28/1997	CLOCK PHASE DETECTING CIRCUIT	KIYANAGI, HIROYUKI
<u>08872624</u>	<u>5987078</u>	150	06/10/1997	CARRIER REGENERATING CIRCUIT	KIYANAGI, HIROYUKI
<u>08962054</u>	<u>6029056</u>	150	10/31/1997	SPACE DIVERSITY RECEIVING APPARATUS	KIYANAGI, HIROYUKI
<u>09187386</u>	<u>6185201</u>	150	11/06/1998	MULTIPLEX RADIO TRANSMITTER AND MULTIPLEX RADIO TRANSMISSION METHOD, MULTIPLEX RADIO RECEIVER AND MULTIPLEX RADIO RECEIVING METHOD, AND MULTIPLEX RADIO TRANSCEIVER AND MULTIPLEX TRANSMISSION/RECEIVING SYSTEM	KIYANAGI, HIROYUKI
<u>09287358</u>	<u>6320734</u>	150	04/07/1999	COMMUNICATION DEVICE	KIYANAGI, HIROYUKI

<u>09584946</u>	<u>6731704</u>	150	06/01/2000	APPARATUS AND BIT-SHIFT METHOD FOR ELIMINATING INTERFERENCE OF CROSS POLARIZATION	KIYANAGI, HIROYUKI
<u>09768679</u>	<u>6871053</u>	150	01/24/2001	WIRELESS COMMUNICATION APPARATUS	KIYANAGI, HIROYUKI
<u>09771229</u>	Not Issued	71	01/26/2001	CLOCK PHASE DETECTING CIRCUIT AND CLOCK REGENERATING CIRCUIT EACH ARRANGED IN RECEIVING UNIT OF MULTIPLEX RADIO EQUIPMENT	KIYANAGI, HIROYUKI
<u>11094783</u>	Not Issued	30	03/30/2005	OFDM transceiver apparatus	KIYANAGI, HIROYUKI

Inventor Search Completed: No Records to Display.

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